

## 1 RECEIVING APPARATUS

FIG. 1

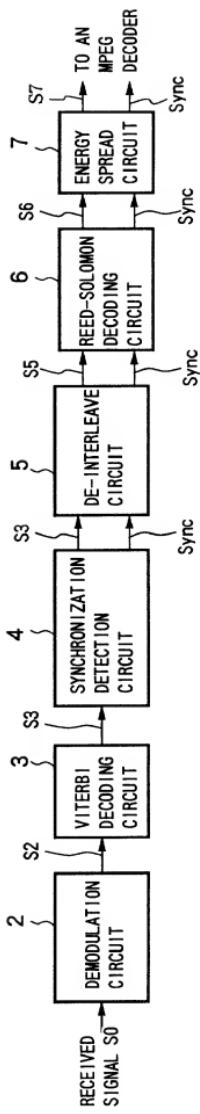
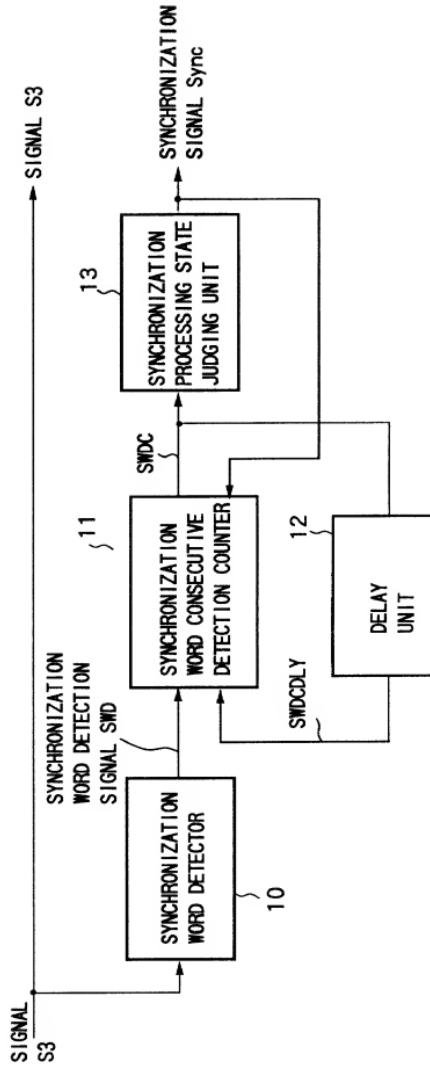
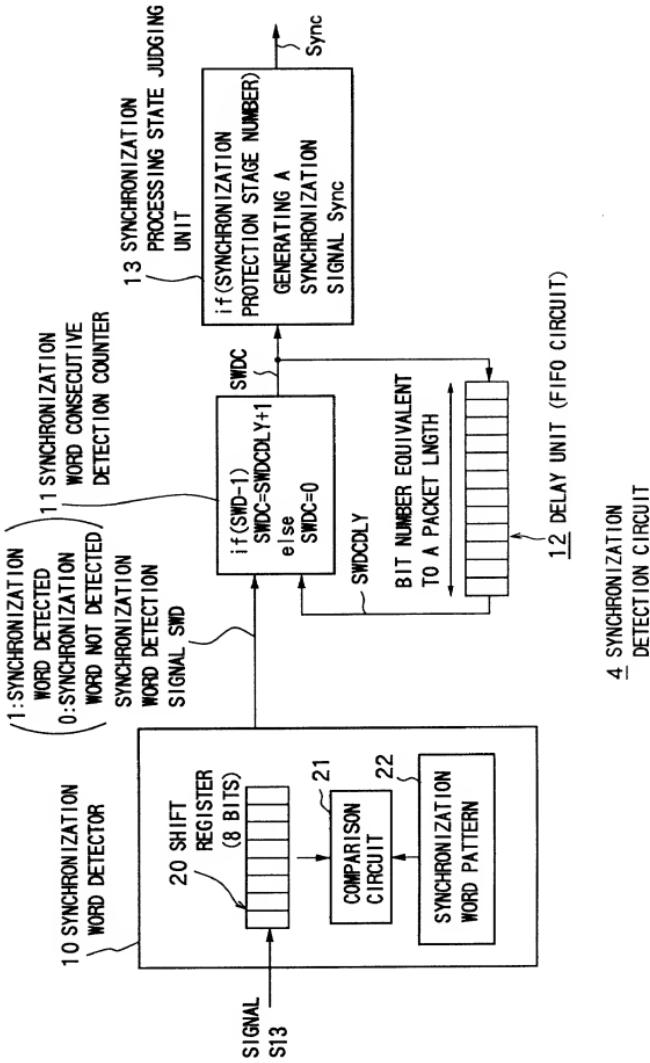


FIG.2



4 SYNCHRONIZATION  
DETECTION CIRCUIT

FIG.3



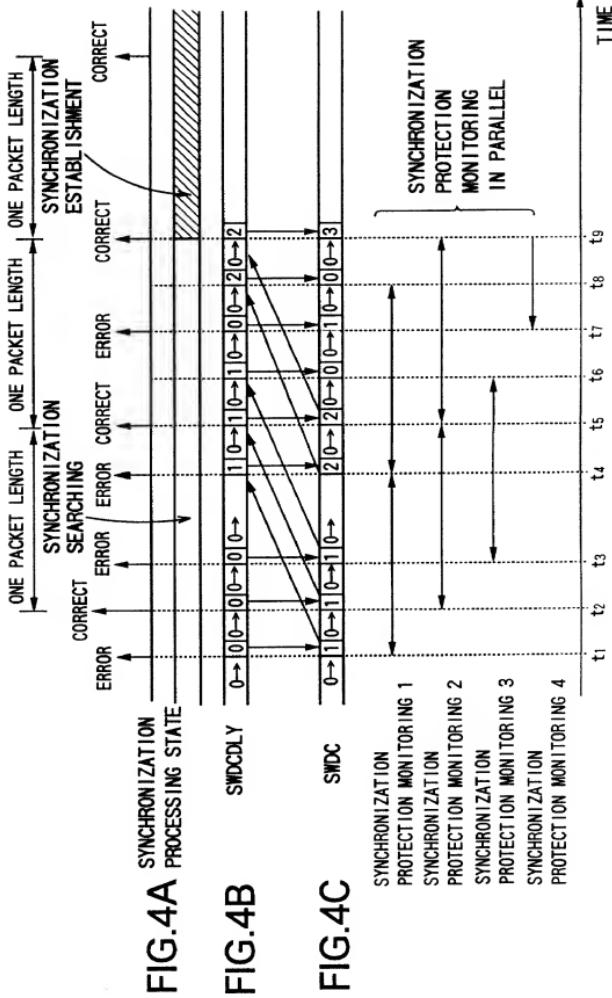
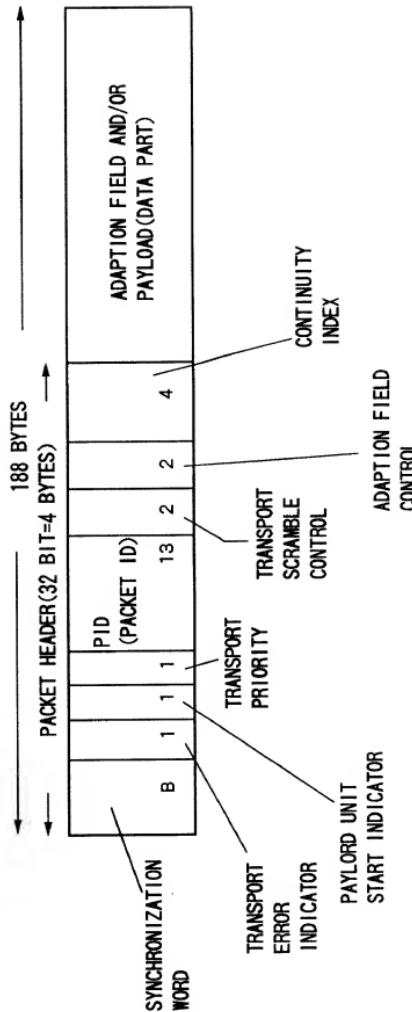


FIG.5



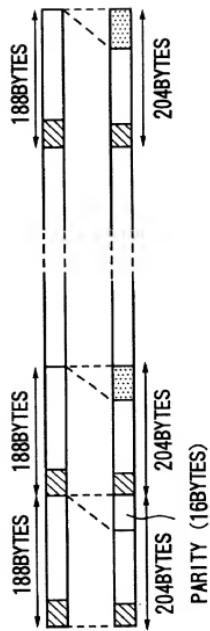
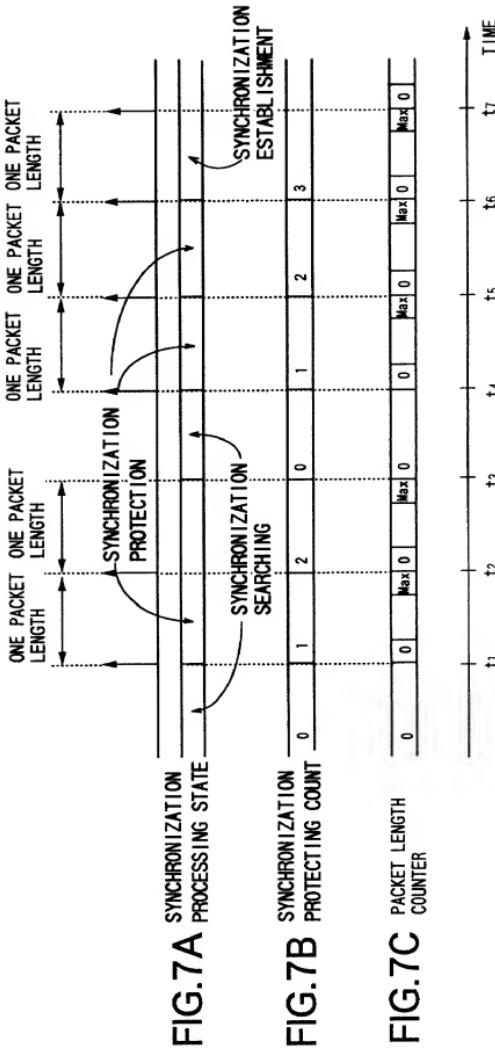


FIG.6A TRANSPORT  
STREAM PACKET

FIG.6B SLOT DATA



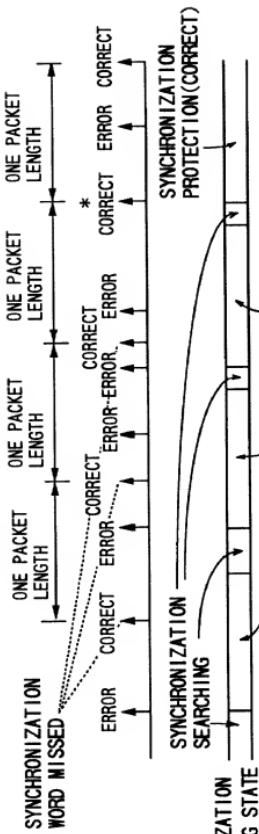


FIG.8A SYNCHRONIZATION PROCESSING STATE



FIG.8B SYNCHRONIZATION PROTECTING COUNT

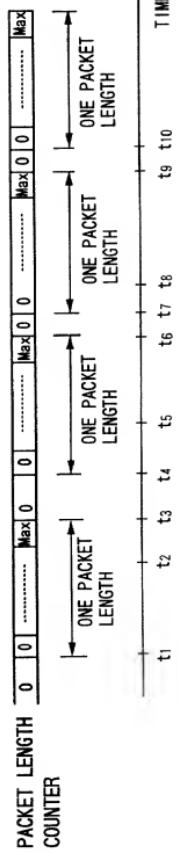


FIG.8C SYNCHRONIZATION PROTECTING COUNT